

DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that: My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled SUPPORT STRUCTURE FOR THINNING SEMICONDUCTOR SUBSTRATES AND THINNING METHODS EMPLOYING THE SUPPORT STRUCTURE, the specification of which (check one):

	is attached hereto. was filed on September was filed on was amended under Pe	as PCT	international applic			,742. ind
	by state that I have reviously including the claims, as					
known to me	owledge the duty to dis to be material to the parts defined in Title 37, C	tentability of th	e subject matter clai	med in this		
365(b) of any international a listed below a attached contiinternational a	by claim foreign priority foreign application(s) foreign application(s) designation and on any attached connuation page any foreign application(s) designations, date before that of the	for patent or inving at least one of tinuation page on application for at least one of the formal at l	ventor's certificate of country other than the and have also identified patent or inventor country other than the	r § 365(a) one United S fied belower's certificate The United S	of any PCT tates of Am and on any te or any Po	nerica CT
Prior foreign/	PCT application(s):			Priority	Claimed	
(numb	er) (c	ountry)	(day/month/year filed)	Yes	No	
(numb	er) (c	ountry)	(day/month/year filed)	Yes	No	
(numb		ountry)	(day/month/year filed)	Yes	No	

application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available

DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention Title: SUPPORT STRUCTURE FOR THINNING SEMICONDUCTOR SUBSTRATES AND THINNING METHODS EMPLOYING THE SUPPORT STRUCTURE

between the filing date of such prior application and the national or PCT international filing date of this application:

(application serial no.)	(filing date)	(status-pending, patented or abandoned)
(application serial no.)	(filing date)	(status-pending, patented or abandoned)
I hereby claim the benefit uprovisional application(s) listed be	•	tes Code, § 119(e) of any United States

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

David V. Trask, Reg. No. 22,012
Laurence B. Bond, Reg. No. 30,549
James R. Duzan, Reg. No. 28,393
Allen C. Turner, Reg. No. 33,041
Kent S. Burningham, Reg. No. 30,453
Kevin K. Johanson, Reg. No. 38,506
Shawn G. Hansen, Reg. No. 42,627
Tawni L. Wilhelm, Reg. No. 47,456
Andrew F. Nilles, Reg. No. 47,825
Katherine A. Hamer, Reg. No. 47,628
Trent N. Butcher, Reg. No. 51,518
Jeffery M. Michelsen, Reg. No. 50,978
Charles B. Brantley II, Reg. No. 38,086

William S. Britt, Reg. No. 20,969
Joseph A. Walkowski, Reg. No. 28,765
H. Dickson Burton, Reg. No. 48,396
Edgar R. Cataxinos, Reg. No. 39,931
Brick G. Power, Reg. No. 38,581
Krista Weber Powell, Reg. No. 47,867
Bretton L. Crockett, Reg. No. 44,632
Bradley B. Jensen, Reg. No. 46,801
Greg T. Warder, Reg. No. 50,208
Marcus S. Simon, Reg. No. 50,258
G. Scott Dorland, Ph.D., Reg. No. 51,622
Michael L. Lynch, Reg. No. 30,871

Address all correspondence to: Brick G. Power, telephone no. (801) 532-1922.

TRASKBRITT, PC P.O. Box 2550 Salt Lake City, Utah 84110

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements

DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention Title: SUPPORT STRUCTURE FOR THINNING SEMICONDUCTOR SUBSTRATES AND THINNING METHODS EMPLOYING THE SUPPORT STRUCTURE

were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Alan G. Wood						
Inventor's signature Date 1/7/04 Residence: Boise, ID						
Citizenship: U.S.A.						
Post Office Address: 1366 E. Versailles Ct., Boise, ID 83706						
1 ost Office Address. 1300 E. Versames Ct., Boise, ID 63700						
Full name of second joint inventor: Warren M. Farnworth						
Inventor's signature and The Date 01-07-04						
Residence: Nampa, ID						
Citizenship: U.S.A.						
Post Office Address: 2004 S. Banner, Nampa, ID 83686						
1 ost Office Address. 2004 S. Dainier, Nampa, 1D 05000						
Full name of third joint inventor: David R. Hembree						
Inventor's signature Naud M. Alla Date 61-07-04						
Residence: Boise, Idaho						
Citizenship: U.S.A.						
Post Office Address: 11074 W. Edgehill Dr., Boise, Idaho 83709						
1 obt office i taurous. 11071 77. Eugenni Bii, Boise, iaano oo 705						
Full name of fourth joint inventor: Sidney B. Rigg						
Inventor's signature Date 01-07-04						
Residence: Meridian, ID						
Citizenship: U.S.A.						
Post Office Address: 2477 N. Leann Way, Meridian, Idaho 83642						
1 Ost Office Address. 2477 IV. Dealiff Way, McHalail, Idaho 83042						
Full name of fifth joint inventor: William M. Hiatt						
And the state of t						
Inventor's signature Date 01.07.04.						
Residence: Eagle, ID /						
Citizenship: U.S.A.						
Post Office Address: 1137 W. Stafford Dr., Eagle, ID 83616						

DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention Title: SUPPORT STRUCTURE FOR THINNING SEMICONDUCTOR SUBSTRATES AND THINNING METHODS EMPLOYING THE SUPPORT STRUCTURE

Full name of sixth joint inventor: Peter Benson					
Inventor's signature Trans Date 01/16/2004 Residence: Boise, ID					
Citizenship: U.S.A.					
Post Office Address: 5122 Surprise Way, Boise, ID 38716					
Full name of seventh joint inventor: Kyle K. Kirby					
Inventor's signature / / / / Date 1/7/04					
Inventor's signature Like K. Long Date 1/7/04					
Residence: Boise, ID					
Citizenship: U.S.A.					
Post Office Address: 10681 N. Sagecrest Place, Boise, ID 83703					
Full name of eighth joint inventor: Salman Akram					
Inventor's signature Salvan Date 1/7/03					
Residence: Boise, ID					
Citizenship: Pakistan USA					
Post Office Address: 1463 E. Regatta St., Boise, Idaho 83706					

Document in ProLaw